



**ECC 2017 Pre-Conference Workshop**  
**z Systems Update & Performance**  
**am 25. April 2017**

**Im Rahmen der Enterprise Computing Conference 2017**  
**im Hotel Crowne Plaza in Berlin**

**Liebe Mitglieder und Interessenten der ceCMG**

Wir werden in diesem Jahr wieder einen „z Systems Performance & Update“ Workshop anbieten.

Es ist uns erneut gelungen, mit **Fabio Ottaviani von EPV** und **Dr. Gilbert Houtekamer von IntelliMagic** zwei international bekannte Top-Sprecher zu verpflichten. Wer sie kennt, weiß, dass wir nicht zu viel versprechen, wenn wir sie beide als „Hochkaräter“ im Mainframe Performance-Umfeld bezeichnen! Die Vorträge von Fabio und Gilbert finden in Englisch statt!

Der Workshop findet am Tag vor der eigentlichen Konferenz im gleichen Tagungshotel, d.h. im Crowne Plaza in Berlin statt.

Workshop und Konferenz können unabhängig voneinander gebucht werden. Wenn Sie beides buchen, profitieren Sie von 20% Rabatt auf den Gesamtpreis.

Buchung unter [www.cecmg.de](http://www.cecmg.de) . Dort finden Sie auch die Anmeldeunterlagen für die **Enterprise Computing Conference 2017**.

Wolfram Greis  
Vorstandssprecher ceCMG

A handwritten signature in black ink, appearing to read 'W. Greis', written in a cursive style.

Im Namen der Vorstandschaft der ceCMG



**Agenda Pre-Conference-Workshop  
„z Systems Update und Performance“  
am 25. April 2017 in Berlin**

**Inhalt:**

- 09.30 – 10.00 Uhr**    Registration & Kaffee
- 10.00 Uhr**            **Begrüßung & Einführung**  
Wolfram Greis, ceCMG  
**zIIP and Memory: the two Pillars of z/OS Cost Reduction**  
Fabio Massimo Ottaviani, EPV Technologies
- 11.15 – 11.30 Uhr**    Kaffeepause
- 11.30 Uhr**            **Processor Reporting from Capture Ratio to RNI**  
Dr. Gilbert Houtekamer, IntelliMagic
- 13.00 – 14.00 Uhr**    Mittagspause
- 14.00 Uhr**            **New Insights in your z/OS Systems**  
Dr. Gilbert Houtekamer, IntelliMagic
- 16.00 – 16.15 Uhr**    Kaffeepause
- 16.15 Uhr**            **Diskussion “Neue Technologien und Mainframes”**  
Impulsvortrag & Moderation:  
Wolfram Greis, ceCMG & EMA
- Ende ca. 17.00 Uhr**



## Abstracts der Vorträge:

### **zIIP and Memory: the two Pillars of z/OS cost reduction**

zIIP and memory have a very important common characteristic: if well exploited these resources may allow a considerable reduction of CPU usage and consequent benefits in terms of hardware and software cost savings.

The pre-requisite of this exploitation is a good capacity planning; it has to take into consideration the current level of utilization, eventual latent demand, future needs both for application and system software evolution in order to provide a correct estimate of the needed zIIP and memory resources.

Unfortunately configurations where the number of zIIPs and the memory available are insufficient are still quite common making it not only impossible reducing the CPU usage and but also causing application performance degradation.

In this presentation, we'll discuss:

- the most important KPI to analyze in order to understand the current utilization of zIIP and memory and estimate an eventual latent demand
- the new possibilities in terms of zIIP and memory exploitation offered by the most recent z/OS technologies to reduce CPU usage.

### **Processor Reporting from Capture Ratio to RNI**

The processor remains the most expensive resource in the data center, both directly with the hardware costs and the indirectly with the MSU based software changes.

All installations use some form of processor reporting, but in many cases this reporting goes back many, many years. Capture Ratios are still interesting, but there is a lot more now.

Today processor performance relies more and more on efficient use of processor cache and it's also important to look at new metrics like the SMF 113 Hardware Counters and the SMF 99.14 topology data.

In this presentation, we will cover processor reporting from Capture Ratio to RNI (Relative Nest Intensity), and show you how understanding these metrics can help you tune your system.

## **New insights in your z/OS Systems**



IBM continues to invest in the new features in their mainframe product line, and these new capabilities are reflected in the SMF and RMF records.

In this presentation, we will cover some of the new measurement capabilities, and why they are important.

Significant new developments that are covered in the presentation are the new transaction support for RMF transaction records, created to facility mobile transaction monitoring.

The SMT support for zIIP is starting point for multithreading support in the mainframes, and it comes with new information for CPs and zIIPs in both processor (RMF 70/72) and job (SMF 30) records.

Not related to new features, but important to be aware of is the new system-wide reporting for Locks, Latches and Enqueues. RMF has always provided ENQ reporting, and now also provides similar details for locks and latches.